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Renne M.F. Wyzykowski

Applicant : Indradeep Ghosh
Application No. : 09/851,708
Filed : May 8, 2001
Title : AUTOMATIC TEST PATTERN GENERATION FOR
FUNCTIONAL REGISTER TRANSFER LEVEL
CIRCUITS USING ASSIGNMENT DECISION DIAGRAMS
Grp./Div. : 2858
Examiner : To Be Assigned
Docket No. : 40704/PYI/F179

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April 16, 2002

Assistant Commissioner for Patents
Washington, D.C. 20231

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Commissioner:

Technology Center 2100

In compliance with the duty of disclosure under 37 CFR §§ 1.56, 1.97 and 1.98, and in accordance with the provisions in the Manual of Patent Examining Procedure §§ 609 and 707.05(b), enclosed is FORM PTO/SB/08A/B listing the references that are known to applicant. Copies of each of the listed references are enclosed. This filing is timely because it is made during one of the periods described in 37 CFR § 1.97(b).

It is respectfully requested that the listed references be considered in the examination of this application and identified on the list of references cited on the patent issuing for this application. Applicant also requests that an initialed copy of FORM PTO/SB/08A/B be entered in the application file and returned to applicant with the next communication from the Office in accordance with MPEP § 609.

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

By

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PYI/rmw

Enclosures: PTO/SB/08A/B, w/references
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